## Code No.: 16341 N

## VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS), HYDERABAD Accredited by NAAC with A++ Grade

## B.E. (E.E.E.) VI-Semester Main Examinations, May/June-2023 **Power Electronics**

Time: 3 hours

Max. Marks: 60

· Note: Answer all questions from Part-A and any FIVE from Part-B

1.	Stem of the question	T.		and the second	
1.	Classify power diodes.	N.	L	C	) ]
2.	Sketch the gate V-I characteristics of thyristor.	2	2	1	1
3.	Define firing angle of thyristor.	2	1	1	1
4.	Give any two applications of phase-controlled rectifiers.	2	1	2	1,
5.	Name the control strategies of choppers.	2	1	2	1,
6.	Define duty cycle.	2	1	3	1,
7.	Compare voltage source inverter and current source inverter.	2	1	3	1,2
8.	ally two advantages of sinusoidal pulse width modules	2	2	4	1,2
9.	Draw the circuit diagram of single-phase half wave-controlled ac voltage	2	1	4	1,2
10.	Which commutation is employed for step-up cycloconverter?	2	1	5	1,2
	Part-B (5 $\times$ 8 = 40 Marks)	2	1	5	1,2
1. a) V	With a neat circuit and relevant waveforms explain the operation of RC half wave triggering circuit for Thyristor.	4	2		
b) F	For the circuit shown below, $V_S = 200 \text{ V}$ , $L = 100 \mu\text{H}$ and $C = 250 \mu\text{F}$ . For a constant load current of 60 A, calculate (a) the peak value of currents through apacitance, main and auxiliary thyristors (b) circuit turn-off times for main and auxiliary thyristors.	4	4	1	1,2
	plain the operation of single-phase full-wave-controlled bridge rectifier with load. Draw the waveforms of source voltage, gate current, load voltage load current for a given firing angle α. Hence obtain expression for average voltage under continuous conduction mode.	4 2	2	1,2	2,3

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	to fod from 400 V. Julia 110	4	4	2	1,2
S	3-phase fully controlled bridge converter is led from 100 yourse and is operating at a firing angle of 60°. The load current is assumed onstant at 10 A due to high load inductance. Calculate the average output voltage, the input displacement factor (IDF) and the input power factor (IPF)				
1	Sthe converter	4	3	3	1,2,3
	With a neat circuit diagram, explain the operation of Buck converter. Also sketch the waveforms of supply voltage, gate signal, inductor voltage, inductor current, capacitor voltage and capacitor current. Derive the expression for mean				
	output voltage.	4	4	3	1,2,3
b)	The boost converter has an input voltage of 23V, the required stopped is 30kHz. If 75V, the average load current is 0.6A and the switching frequency is 30kHz. If the filter inductance and the filter capacitance values are 350µH and 500µF respectively, determine (a) the duty cycle (b) ripple current of the inductor (c) the ripple voltage of filter capacitor (d) critical values of L and C.				
14 ->	the ripple voltage of filter capacitor (d) creating inverter with 120° conduction	4	2	4	1,2
14. a)	devices in six intervals in one cycle). Also sketch the waveforms of six gate devices in six intervals in one cycle). Also sketch the waveforms of six gate devices in six intervals in one cycle). Also sketch the waveforms of six gate devices in six intervals in one cycle). Also sketch the waveforms of six gate devices in six intervals in one cycle).				
	period of about two cycles (120).	4	4	4	1,2,3
b)	input voltage of 220 V de, Calculate (a) Determine THD of the inverter.				1,2
15. a)	Explain the operation of a single-phase full wave ac voltage controller recently an R-L load. Also sketch the waveforms of source voltage, gating signals, an R-L load. Also sketch the waveforms of source voltage, gating signals, and restriction output current and voltage across one SCR.	4	2		- 31
b	A single phase voltage controller has input voltage of 230 V, 50 Hz and a load of $R = 15 \Omega$ . For 6 cycles ON and 4 cycles OFF, determine (i) the RMS load of $R = 15 \Omega$ . For 6 cycles ON and 4 cycles OFF, determine (ii) the input power factor (iii) the average thyristor current.	4	2		
16. a	Describe the different modes of operation of a thyristor with help of its states	4		2	1 1,2
	V-I characteristics.	4		4	2 1,2,3
	of the following:				2 121
17.	Also sketch the waveforms of supply voltage, gate signal, inductor voltage inductor current, capacitor voltage and capacitor current. Derive the expression of the control of the control of the capacitor current.	on	4	3	3 1,2,3
	b) Explain the operation of single phase half bridge voltage source inverter wi	1	4	2	4 1, <sup>2</sup> 5 1,2
	c) With a neat circuit and relevant waveforms, explain the operation of a sing	gle ne	4	2	3 1,2
	third of the input frequency.	: Pros	gramm	ne Outc	ome
	1. Bloom's Taxonomy Level; Co, Course	20%			
	M: Marks; L. Blooms Taxonomy Level – 1	100/		1 100	

the input reques	- ~ 0-4-0-0'	PO: Programme Outcom			
s; L: Bloom's 7	axonomy 20.	20%			
i)	Blooms Taxonomy Level – 1	40%			
ii)	Blooms Taxonomy Level – 2	40%			
iii)	Blooms Taxonomy Level – 3 & 4.	10/0			

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